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06/20/01

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:  
B. Jagannathan et al.

Group No.: not yet assigned

Serial No.:

Examiner: unknown

Filed: Concurrently

For: A NON-SELF-ALIGNED SiGe HETEROJUNCTION BIPOLAR TRANSISTOR

Asst. Commissioner for Patents  
Washington, D.C. 20231

INFORMATION DISCLOSURE STATEMENT  
UNDER 37 C.F.R. 1.56

Sir:

Under provisions of 37 C.F.R. 1.97 through 1.99 and pursuant to applicant's duty of disclosure under 37 C.F.R. 1.56, applicants respectfully bring the documents listed on the attached Form PTO-1449 to the attention of the Examiner in charge of the above-identified application. A copy of each listed document is provided herewith for the convenience of the Examiner.

These documents are being cited within three months of the application filing date and/or before a first office action on the merits has issued.

This citation does not constitute an admission that the cited references are relevant or material to the claims nor should it be construed as a representation that no other art than that identified exists. They are merely cited as constituting related art of which the applicant is aware.

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DATE OF DEPOSIT: 6/20/01

BY SIGNATURE & DATE: Karen G. Mans 6/20/01

It is respectfully requested that these documents be considered by the Examiner and formally made of record in this application.

Respectfully submitted,



Samuel W. Ntiros

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